

PART NUMBER:

KXM60-1138

36 Thornwood Drive			APP	PROVED BY		DATE	
Ithaca, New York 14850 Tel: 607-257-1080 Fax: 607-257-1146 www.kionix.com			PROD. MGR.	Scott	Miller	1/22/07	
			TECH. MGR.	Ken I	oust	1/22/07	
			TEST MGR.	John (Chong	1/22/07	
			VP ENG.	Tim [Davis	1/22/07	
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Product Description

The KXM60-1138 is a single-axis, analog-output, silicon micromachined accelerometer with a fullscale output range of $\pm 5g$ (49.0m/s²). The sense element is fabricated using Kionix's proprietary plasma micromachining process technology. Acceleration sensing is based on the principle of a differential capacitance arising from acceleration-induced motion of the sense element, which further utilizes common mode cancellation to decrease errors from process variation, temperature, and environmental stress. The sense element is hermetically sealed at the wafer level by bonding a second silicon lid wafer to the device using a glass frit. A separate ASIC device packaged with the sense element provides signal conditioning and self-test. The accelerometer is delivered in an overmolded, 16 pin SOIC wide body plastic package operating from a 2.7 – 5.25V DC supply.

Product Specifications

Parameters	Units	Min	Target	Max
Operating Temperature Range	°C	-40	-	125
Supply Voltage (V _{dd})	V	4.75	5.00	5.25
Minimum Output Voltage ¹	V	0.08 * V _{dd}	0.10 * V _{dd}	0.12 * V _{dd}
Maximum Output Voltage ¹	V	0.88 * V _{dd}	0.90 * V _{dd}	0.92 * V _{dd}
Zero-g Offset @ RT (25°C) ²	V	0.48 * V _{dd}	0.50 * V _{dd}	0.52 * V _{dd}
Zero-g Offset Error (-40125°C) ³	mg	-100	-	100
Sensitivity @ RT (25°C, V _{dd} = 5V)	mV/g	395	400	405
Sensitivity @ RT (25ºC)	mV/g	79 * V _{dd}	80 * V _{dd}	81 * V _{dd}
Sensitivity Error (-40…125°C) ⁴	%	-1.25	-	1.25
Offset Ratiometric Error ($V_{dd} = 5V \pm 5\%$)	% of FS	-	0.4	1.0
Sensitivity Ratiometric Error ($V_{dd} = 5V \pm 5\%$)	% of FS	-	1.4	1.75
Non-Linearity	%	-	0.1	0.5
Mechanical Over Range Stop (x,y)	g	65	75	85
Cross Axis Sensitivity	%	-	2.0	3.0
Self Test Output on Activation	g	0.9	1.1	1.3
Bandwidth (-3dB) ⁵	Hz	-	250	3100
Noise Density	μg/rtHz	-	40	60
Current Consumption (V _{dd} = 5V)	mA	-	3.9	5.0
Output Resistance	kΩ	64	80	96
Load Resistance	kΩ	5	10	-
Load Capacitance	pF	-	-	1000
Storage Temperature Range	°C	-55	-	150
Mech. Shock (w/ & w/o power)	g	-	-	3500
Drop test to concrete	m	-	-	1.2
ESD (human body model)	V	-	-	3000

Notes

¹ Output saturates at these values - readings outside this range indicate fault.

² Zero-g offset target applies with vertical programming orientation.



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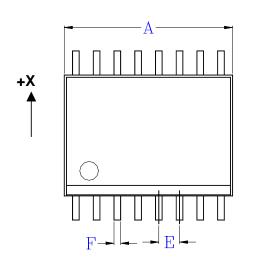
³ Zero-g offset error specified as $(Vout(0g) - V_{dd}/2) / Vsens [g]$ (room temp. error included); Vsens = Target sensitivity. ⁴ Sensitivity error specified as ({[Vout(+1g) + Vout(-1g]] / 2} - Vsens) / Vsens *100% [%] (room temp. error included); Vsens =

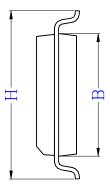
Package Type: Overmolded 16-pin SOIC wide body package

Target sensitivity.

 5 User definable with external capacitors. Maximum bandwidth limited by the mechanical natural frequency of the sensor = 3100 ± 200 Hz.

Package Dimensions







Dimension	Inches	Millimeters
Α	0.406	10.31
В	0.296	7.52
С	0.043	1.09
D	0.096	2.44
Е	0.050	1.27
F	0.016	0.41
G	0.007	0.18
Н	0.406	10.31
Ι	0.103	2.62

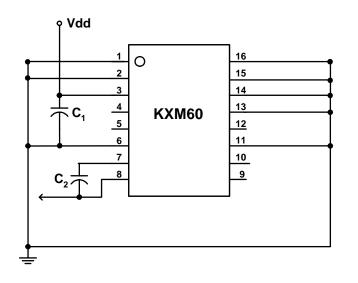
Note: When the device is accelerated in the +X direction, the output will increase.



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Application Schematic



Pin	Internal Function	Recommended Connection
1	N/C	GND
2	GND	GND
3	Vdd	Vdd
4	Reserved	DNC
5	Self Test	Self Test
6	Reserved	GND
7	X filter	X filter
8	Output X	Output X
9	Reserved	DNC
10	Reserved	DNC
11	Reserved	GND
12	Parity	Parity
13	Reserved	GND
14	Reserved	GND
15	N/C	GND
16	N/C	GND

Performance Notes

- Recommend using 0.1 µF for the decoupling capacitor (C1).
- A 1000 pF capacitor from the output to ground will reduce high frequency noise.
- Pin 2 must be power supply ground. Recommend grounding pins 1, 6, 11, 13, 14, 15, and 16.
- N/C Not Connected, DNC Do Not Connect
- Parity Checks EEPROM for parity error. Output high on this pin indicates a parity error.
- The output of a properly functioning part will increase when 5V is applied to the self test pin (#5).
- The bandwidth can be adjusted with a capacitor (C_2) across pins 7 and 8. The response is single pole. Given a desired bandwidth, f_{BW} , the filter capacitor, C_{BW} , is determined by:

$$C_2 = C_{BW} = \frac{1.99 \times 10^{-6}}{f_{BW}}$$

The startup time and self test response time are governed by the filter capacitor chosen and the output resistance of the accelerometer (R_{out}). The following equation can be used to calculate startup time, t_{start} (in seconds):

$$t_{start} = 5 * R_{out} * C_{BW}$$



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Test Specifications

Special Characteristics:

These characteristics have been identified as being critical to the customer. Every part is tested to verify its conformance to specification prior to shipment.

Parameters	Specification	Test Conditions
Zero-g Offset @ RT	2.5 ± 0.1 V	$25^{\circ}C, V_{dd} = 5.0V$
Zero-g Offset Error (-40125°C)	± 100 mg	-40°C, 25°C, 125°C; V _{dd} = 5.0V
Sensitivity @ RT	400 ± 5 mV/g	25°C, V _{dd} = 5.0V
Sensitivity Error (-40125°C)	± 1.25%	-40°C, 25°C, 125°C; V _{dd} = 5.0V
Offset Ratiometric Error	< 1% of FS	$V_{dd} = 5V \pm 5\%$
Sensitivity Ratiometric Error	< 1.75% of FS	$V_{dd} = 5V \pm 5\%$
Self Test Output on Activation	1.1 ± .2 g	25°C, V _{dd} = 5.0V
Non-Linearity	< 0.5%	25°C, V _{dd} = 5.0V